

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

Zero Static Power Fuse Cell for Integrated Circuits

Application Number:

10/042702

Confirmation Number:

2428

First Named Applicant:

Fan Yung Ma

Attorney Docket Number: 2000P13206US Art Unit:

1745

Search string:

(4546455 or 4922134 or 5345110 or 5929691

or 6021067 or 6087890 or 6201750 or 6215351

or 6236599 or 6256239 or 6281739 or

6404680).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
W	1	4546455	1985-10-08	lwahashi et al		<u>.</u>	
F2	2	4922134	1990-05-01	Hoffmann et al			
42	3	5345110	1994-09-06	Renfro et al			
HV)	4	5929691	1999-07-27	Kim et al			
图	5	6021067	2000-02-01	Ha			
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H	7	6201750	2001-03-13	Busch et al			
H	8	6215351	2001-04-10	Le et al	•		
HN	9	6236599	2001-05-22	Goto			
EN	10	6256239	2001-07-03	Akita et al			
HN	11	6281739	2001-08-28	Matsui			
图	12	6404680	2002-06-11	Kwon			

Signature

Examiner Name	Date
HIEN NGUYEN	3/6/04

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PTO/SB/08A (10-01)

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		Y APPLICANT		First Named Inventor	Fan Yung MA	
				Art Unit	1745	
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Sheet	1	of	2	Attorney Docket Number	00P13206US	

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No.1	Document Number Number - Kind Code (Flanown)	Date of Patent MM-DD-YYYY	Name of Patentee or Applicant of Cited Decument	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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W		EP 0 528 744 A2	Feb 24, 1993	International Business Machines Corporation		
47		EP 0 528 744 A3	Feb 24, 1993	International Business Machines Corporation		Ŀ
HN		EP 0 947 993 A2	Oct 06, 1999	Xerox Corporation		
157		EP 0 947 993 A3	Jun 21, 2000	Xerox Corporation_		Ľ
127		JP 2000021191	Jan 21, 2000	NEC Corporation		
HN		JP 58105497	Jun 23, 1983	Toshiba Corporation		匚

Examiner Signature	HIEN	NGUYEN	Date Considered	3/6/04
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				Art Unit	1745	
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		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Exeminar Initials	Cits No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the tean (book, registers, journel, sertal, symposium, setales, sys.), dyb., page(s), volume-issue number(s), publisher, dry and/or country where published. "DC CMOS FUSE TRUE/COMPLEMENT GENERATOR", IBM Technical Disclosure Builletin; Volume 30; No.: 12:	7.
HN.		"DC CMOS FUSE TRUE/COMPLEMENT GENERATOR", 18M Technical Disclosure Bulletin; Volume 30; No.: 12: May 1988; Pp. 51	
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